Application of FCL & SFCL in Distributed Generated System

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Abstract – Distributed Generation Resources are increasingly used in distribution systems due to their great advantages. The presence of DG, however, can cause various problems such as miss-coordination, false tripping, blinding and reduction of reach of protective devices. Using superconducting fault current limiters (SFCLs) is one of the best methods to minimize these problems comparing to the other conventional methods. The active SFCL can as well suppress the short-circuit current induced by a three-phase grounded fault effectively, and the power system’s safety and reliability can be improved and it is composed of an air-core superconducting transformer and a PWM converter. The magnetic field in the air-core can be controlled by adjusting the converters output current, and then the active SFCLs equivalent impedance can be regulated for current limitation and possible overvoltage suppression. During the study process, in view of the changes in the locations of the DG units connected to the system, the DG units injection capacities and the fault positions, the active SFCLs current-limiting and over voltages suppressing characteristics are presented by using Matlab/Simulink software. In extension the active type SFCL is replaced with resistive type SFCL and it is test under different fault conditions by using MATLAB/SIMULATION software.

Keywords: Resistive type SFCL, Active type SFCL, Distribution generation, over voltages, Fault conditions

I. INTRODUCTION

In recent years, with the great development of interconnected power grid, the power network structure becomes increasingly complicated, and the system short circuit capacity and short circuit current have reached a new level which could exceed the allowable currents of the circuit breakers. The increase of the fault current has imposed a severe burden on the related machinery in the grid, and the stability of the power system is also damaged. The fault current limiters (FCL) are regarded as the suitable solution to solve excessive fault current problems. [1] Active superconducting fault current limiter (ASFCL) voltage compensation type is a novel topology of FCL. This type SFCL not only preserves the merits of bridge type SFCL such as the automatic switch to the current limiting mode and without the quench of the superconductor, but also has the particular abilities of controlling the steady fault current and compensating active and reactive power for AC main circuit in the normal state. Fig. 1 shows the circuit structure of the three phase active SFCL, which is consisting of three air-core superconducting transformers and a three-phase voltage source converter.

A single phase ground fault happens in a decentralized system with isolated neutral, fault voltages will be induced on the other two health phases, and considering the multiple decentralized generating units the impact of the fault voltages on the system. Insulation stability and operation safety should be taken in to account seriously. The problem is taking in to consideration, applying superconducting fault current limiter (SFCL) may be an accurate solution. The superconducting elements mainly focused on the current limitation and improve the protection co-ordination [3] among the devices. The impact of the superconducting material on fault voltages is less compared to current .the magnitude of fault current is decreased to minimum value, these are depends on the length and operating temperature. If operating temperature exceeded, the SFCL comes in to operation the fault current bypass through resistor. Active superconducting fault current limiter (ASFCL) voltage compensation type is a novel topology of FCL. This type SFCL not only preserves the merits of bridge type SFCL such as the automatic switch to the current limiting mode and without the quench of the superconductor, but also has the particular abilities of controlling the steady fault.
current and compensating active and reactive power for AC main circuit in the normal state.

II. ANALYSIS OF ACTIVE TYPE AND RESISTIVE TYPE SFCL

A. Structure and Principle of the Active SFCL

As shown in Fig. 1(a), it denotes the circuit structure of the single-phase voltage compensation type active SFCL, which is composed of an air-core superconducting transformer and a voltage-type PWM converter. \( Ls1, Ls2 \) are the self-inductance of two superconducting windings, and \( M_s \) is the mutual inductance. \( Z_1 \) is the circuit impedance and \( Z_2 \) is the load impedance. \( Ld \) and \( C_d \) are used for filtering high order harmonics caused by the converter. Since the voltage-type converter’s capability of controlling power exchange is implemented by regulating the voltage of AC side, the converter can be thought as a controlled voltage source \( U_p \). By neglecting the losses of the transformer, the active SFCL’s equivalent circuit is shown in Fig. 1(b).

![Fig.1. Single-phase voltage compensation type active SFCL. (a) Circuit structure and (b) equivalent circuit.](image)

In normal (no fault) state, the injected current \( I_2 \) in the secondary winding of the transformer will be controlled to keep a certain value, where the magnetic field in the air-core can be compensated to zero, so the active SFCL will have no influence on the main circuit. When the fault is detected, the injected current will be timely adjusted in amplitude or phase angle, so as to control the superconducting transformer’s primary voltage which is in series with the main circuit, and further the fault current can be suppressed to some extent. Below, the suggested SFCL’s specific regulating mode is explained. In normal state, the two equations can be achieved.

\[
\begin{align*}
\hat{U}_s &= \hat{I}_1 (Z_1 + Z_2) + j \omega L_{q1} \hat{I}_1 - j \omega M_s \hat{I}_2 \\
\hat{U}_s &= j \omega L_{q1} \hat{I}_1 - j \omega L_{q2} \hat{I}_2
\end{align*}
\]

(1) \hspace{1cm} (2)

Controlling \( I_2 \) to make \( j \omega L_1 I_1 - j \omega M_s I_2 = 0 \) and the primary voltage \( U_1 \) will be regulated to zero. Thereby, the equivalent limiting impedance \( Z_{SFCL} \) is zero (\( Z_{SFCL} = U_1/I_1 \)), and \( I_2 \) can be set as \( I_2 = k \) \( U_s/L_{s1}/L_{s2} \) (\( Z_1 + Z_2 \)), where \( k \) is the coupling coefficient and it can be shown as \( k = M_s/\sqrt{L_{s1}L_{s2}} \). Under fault condition (\( Z_2 \) is shorted), the main current will rise from \( I_1 \) to \( I_1f \), and the primary voltage will increase to \( U_1f \).

\[
\hat{I}_{1f} = \frac{\hat{U}_s + j \omega M_s \hat{I}_2}{(Z_1 + j \omega L_{q1})} \hspace{1cm} (3)
\]

\[
\hat{U}_{1f} = \frac{U_{c}(j \omega L_{q1}) - L_{s2} (j \omega M_s)}{Z_1 + j \omega L_{q1}} \hspace{1cm} (4)
\]

The current-limiting impedance \( Z_{SFCL} \) can be controlled in:

\[
Z_{SFCL} = \frac{\hat{v}_{1f}}{\hat{i}_{1f}} = j \omega M_s/\sqrt{Z_1(L_{s1} + j \omega L_{q1})} \hspace{1cm} (5)
\]

![Fig.2. Application of the active SFCL in a distribution system with DG units.](image)

According to the difference in the regulating objectives of \( I_2 \), there are three operation modes:

1) Making \( I_2 \) remain the original state, and the limiting impedance \( Z_{SFCL} = Z_2 \) (\( j \omega L_{s1} \) to \( (Z_1 + Z_2) \)).

2) Controlling \( I_2 \) to zero, and \( Z_{SFCL} = j \omega L_{s1} \).

3) Regulating the phase angle of \( I_2 \) to make the angle difference between \( U_s \) and \( j \omega M_s I_2 \) be 180°. By setting \( c = \omega L_{s1}/(L_{s1} + j \omega M_s) \), and \( Z_{SFCL} = c Z_1/(1 - c) \).

The air-core superconducting transformer has many merits, such as absence of iron losses and magnetic saturation, and it has more possibility of reduction in size, weight and harmonic than the conventional iron-core superconducting transformer. Compared to the iron-core, the air-core can be more suitable for functioning as a shunt reactor because of the large magnetizing current and it can also be applied in an inductive pulsed power supply to decrease energy loss for larger pulsed current and higher energy transfer efficiency.

There is no existence of transformer saturation in the air-core, and using it can ensure the linearity of ZSFCL well.

B. Applying the SFCL into a Distribution Network with DG

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As shown in Fig. 2, it indicates the application of the active SFCL in a distribution network with multiple DG units, and the buses B-E are the DG units’ probable installation locations. When a single-phase grounded fault occurs in the feeder line 1 (phase A, k1 point), the SFCL’s mode 1 can be automatically triggered, and the fault current’s rising rate can be timely controlled. Along with the mode switching, its amplitude can be limited further. In consideration of the SFCL’s effects on the induced overvoltage, the qualitative analysis is presented. In order to calculate the over voltages induced in the other two phases (phase B and phase C), the symmetrical component method and complex sequence networks can be used, and the coefficient of grounding G under this condition can be expressed as:

\[ G = -1.5/m(2 + m) \pm j \sqrt{3}/2, \]

where \( m = X0/X1 \), and \( X0 \) is the distribution network’s zero-sequence reactance, \( X1 \) is the positive-sequence reactance. Further, the amplitudes of the B-phase and C-phase overvoltages can be described as:

\[ U_{BO} - U_{CO} - \sqrt{3} \left( \frac{\sqrt{G^2 + G + 1}}{G + 2} \right) U_{AN} \]  

Where \( U_{AN} \) is the phase-to-ground voltage’s root mean square (RMS) under normal condition. As shown in Fig. 3, it signifies the relationship between the reactance ratio \( m \) and the B-phase overvoltage. It should be pointed out that, for the distribution system with isolated neutral-point, the reactance ratio \( m \) is usually larger than four. Compared with the condition without SFCL, the introduction of the active SFCL will increase the power distribution network’s positive-sequence reactance under fault state. Since \( X0/(X1 + Z_{SFCL}) < X0/X1 \), installing the active SFCL can help to reduce the ratio \( m \). And then, from the point of view of applying this suggested device, it can lower the overvoltage’s amplitude and improve the system’s safety and reliability. Furthermore, taking into account the changes in the locations of the DG units connected into the distribution system, the DG units’ injection capacities and the fault positions, the specific effects of the SFCL on the fault current and overvoltage may be different, and they are all imitated in the simulation analysis.

Fig. 3. The Current Waveform With and Without SFCL during A Fault.

III. FAULT CURRENT LIMITER

Fig. 5 shows the circuit topology of the proposed FCL which is composed of the two following parts:

1) Bridge part that includes a diode rectifier bridge, a small dc limiting reactor. (Note that its resistance is involved too.), a semiconductor switch (IGBT or GTO), and a freewheeling diode.
2) Shunt branch as a compensator that consists of a resistor and an inductor.

![Fig.5. Proposed FCL topology](image)

The total power losses of the proposed structure become a very small percentage of the feeder’s transmitted power.

1) It is possible to placing a small reactor in series with the self turn off switch at dc side. This reactor plays two roles; Snubber for self turn off switch to protect it and current limiter at first moments of fault that will be discussed in detail.

2) In normal operation of power system, self turn off switch is ON and small dc reactor is charged to the peak of line current and behaves as short circuit. Neglecting small voltage drop on diodes and self turn off switch, total voltage drop on FCL becomes almost zero and therefore, FCL does not affect normal operation of power system. As fault occurs, dc reactor limits increasing rate of short circuit current and starts to charge. When the line current reaches to the pre-defined value that can be set by system operator, control system turns off the self turn off switch. So, the bridge retreats from utility. At this moment, free wheeling diode turns on and provides free path for discharging the dc reactor. When the bridge turns off, fault current passes through the resonance part of the proposed FCL. Without using arrester in the proposed topology, only series LC resonance circuit will be in fault.
current path. As will be shown in analytical analysis section, in such condition, fault current will has increasing variations. But, by using arrester fault current can be limited to a constant value. In addition, it is possible to protect the capacitor from over voltages that can be harmful.

By removal of fault, self turn off switch turns on again and system returns to the normal state.

IV. MATLAB/SIMULINK RESULTS

Case 1: Active type SFCL

![Fig.6. Matlab/Simulink Model of Proposed Distribution System with Distributed Generation Units without any compensation scheme using Active SFCL methodology, using Matlab/Simulink platform.](image)

![Fig.7. Voltage characteristics of the Bus-A under different locations of DG without SFCL.](image)

![Fig.8. Voltage characteristics of the Bus-A under different locations of DG with SFCL.](image)

![Fig.9. Line current waveforms when the three-phase short-circuit occur at k3 point without SFCL.](image)

![Fig.10. Line current waveforms when the three-phase short-circuit occur at k3 point with SFCL.](image)

![Fig.11. Active SFCL’s current-limiting performances under different fault locations at k1 point.](image)
DG based operation to enhance the power quality concerns at bus levels using a supportive source with active SFCL and FCL topologies. A single-phase grounded fault, compare to the active type SFCL and active type FCL. Active type FCL is reduces the overvoltage’s amplitude and avoids damaging the relevant distribution equipment. The active FCL can as well suppress the short-circuit current induced by a three-phase grounded fault effectively, and the power system’s safety and reliability can be improved.

REFERENCES