Analysis of Unit vector and Proportional-Resonant Current Controller for Harmonic Compensation in a Hybrid Active Power Filter

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Abstract—
Active filtering of electric power has now become a mature technology for harmonic and reactive power compensation in ac networks. This paper proposes a combination of low- and high frequency hybrid active power filter (APF) to operate in parallel for better performance. The individual hybrid Active Power Filter is a series combination of passive filter with the corresponding voltage source inverter. The dc links of both inverters are connected in parallel, and the voltage of this dc link is maintained by the low-frequency inverter (LFI). The low- and high-frequency inverters eliminate lower and higher order harmonics respectively. In addition, it is possible to design the LFI such that it can also compensate the reactive power of the load. The individual passive filter of the hybrid topology is designed to take care of specific order of harmonics that are predominant in the load. In order to make the supply currents sinusoidal, an effective harmonic compensation method is developed with the aid of a conventional proportional-integral (PI) and vector PI controllers. The absence of the harmonic detector not only simplifies the control scheme but also significantly improves the accuracy of the APF, since the control performance is no longer affected by the performance of the harmonic tracking process. Furthermore, the total cost to implement the proposed APF becomes lower, owing to the minimized current sensors and the use of a four-switch three-phase inverter. Despite the simplified hardware, the performance of the APF is improved significantly compared to the traditional control scheme.

Index Terms-- active power filter (APF); PI controllers AND Shunt Active Power filter

I. INTRODUCTION
The controller design is equally important for an improved performance of an APF. A comparative assessment for different type of controllers including multiple rotating integrators, stationary frame generalized integrators, proportional-sinusoidal integrators, and vector proportional-integral controllers are reported. An outer voltage loop and an inner current loop are necessary for implementation of such linear current control scheme. An nonlinear control technique utilizing two inner current loops and an outer dc bus voltage loop is also proposed. The inner and outer loops are decoupled, and the system took about 1.5 cycles for the outer loop to converge. Passive filters have the advantages of low cost and losses; however, they have the problems of harmonic resonance with the source and/or the load. Moreover, they need to be tuned properly to take care of a wider frequency range. Active filter may completely replace the passive counterpart. This requires higher voltage/current switches for medium/high power applications. Use of hybrid filter, where a lower rating active filter is added in series with the passive filter, has the merit of operating the active filter at a convenient voltage and current.

Later researchers developed a dual hybrid configuration where the series filters are tuned to eliminate fifth and seventh current harmonics. Reduced switch topologies have the advantages of more reliability and less cost and complexity. Reduced switch APF with one cycle control is also proposed. The third leg of the inverter is eliminated, and the third phase is connected to the midpoint (derived by voltage splitting capacitors) of the dc bus.
This topology has the problem of voltage balancing across the dc link capacitors. This is later improved by connecting the where third phase to the negative pole of the dc link.

Hybrid APF with series resonant networks tuned at different harmonic frequencies are reported and extended the application of an APF to PV cells. A different type of hybrid filter is proposed that uses a series passive filter and a thyristor controlled reactor-based variable impedance shunt passive filter to compensate for the harmonics and reactive power. Such systems have relatively poor dynamic performance and are less suitable for highly dynamic types of load.

In addition, different hybrid active power filter (HAPF) topologies composed of active and passive components in series and/or parallel have been proposed, aiming to improve the compensation characteristics of PPFs and reduce the voltage and/or current ratings (costs) of the APFs, thus leading to improvements in cost and performance [2]. The HAPF topologies consist of many passive components, such as transformers, capacitors, reactors, and resistors, thus increasing the size and cost of the whole system. A transformer less shunt hybrid active power filter (SHAPF) has been recently proposed and applied for current quality compensation and damping of harmonic propagation in distribution power systems [12], in which it has only a few passive components.

II. SYSTEM CONFIGURATION

Fig.1 shows the proposed circuit configuration. In this system, a nonlinear load is supplied by a balanced voltage source and compensated by the proposed HAPF. A ripple filter is used to reduce the high-frequency harmonic currents injected into the network.

A. Rating Analysis

The active filter has to provide a small component of fundamental voltage at the PCC in order to divert the fundamental reactive current to flow in the inductor. This voltage is equal to the voltage drop on the inductor for a pure passive filter and it depends on the tuned frequency of the passive part. It can be expressed as

\[
u_{af,1} = u_{s,1} \frac{1}{1 - \left( \frac{f_t}{f_1} \right)^2}
\]  

(1)
Here, is the fundamental frequency (i.e., 50 Hz) and is the tuned frequency of the passive filter.

The harmonic voltage across the active part of the hybrid filter consists of two components: the component due to the distorted supply voltage \( u_{sh} \) and the component due to the harmonic load current \( i_{sh} \) flowing in the passive impedance. This voltage is given by

\[
u_{afh} = u_{sh} + j \cdot i_{sh} \cdot X_{Ch}.
\]

To obtain the worst case, no load impedance was considered when deriving (2), and the coupling impedance of the active

Table IV: Example Case Parameters

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Amplitude</th>
<th>Angle</th>
</tr>
</thead>
<tbody>
<tr>
<td>( U_{s1} )</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>( U_{s2} )</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>( I_{1} )</td>
<td>1</td>
<td>( \pi/4 )</td>
</tr>
<tr>
<td>( I_{5} )</td>
<td>0.2</td>
<td>5 \cdot \pi/4</td>
</tr>
<tr>
<td>( I_{7} )</td>
<td>0.14</td>
<td>7 \cdot \pi/4</td>
</tr>
<tr>
<td>( f_{r} )</td>
<td>240 Hz</td>
<td></td>
</tr>
</tbody>
</table>

Fig.3. Active filter rated power versus tuned frequency of the passive part

Filter was neglected and the harmonic current flowing in the system was considered to be zero (ideal filtering). The current flowing in the active part also consists of two parts: the harmonic component due to the distorted supply voltage and the distorted load current. It is given by

\[
i_{afh} = u_{sh} \cdot \frac{j \cdot f_{i}^2}{f_{h} f_{1} X_{C1}} + i_{sh} (1 - \frac{f_{i}^2}{f_{h}^2})
\]

The power-rating requirement of the active part is finally given by

\[
S_{af} = U_{af} \cdot I_{af}
\]

For this case, the requiredPower of the active part is given by

\[
S_{AF} = U_{AF, rms} \cdot I_{AF, rms}
\]

As is clear from Fig.3, the power rating strongly depends on the tuned frequency of the passive filter. The hybrid active filter of the proposed topology is thus the most suitable for tuned filter branches, where the tuned frequency is as close as possible to the filtered harmonic. For the same load conditions, the rating of the active part for the series HAPF is equal to 0.0251 p.u., while the required rating of the pure active filter for this case is 0.2 p.u. (which does not include the reactive power compensation). An extensive overview of the power-rating requirements for most common current-sink HAPF topologies can be found.

III. CONTROL SYSTEM DESIGN

Proportional-resonant (PR) controllers are equivalent to conventional PI controllers implemented in the reference frame, separately for the positive and negative sequences. Therefore, the PR controller is capable of simultaneously tracking the reference for the positive and negative sequence with zero steady state error. For example, a sixth harmonic PR compensator is effective for the fifth and seventh harmonics of both sequences; hence, four harmonics are filtered with one PR filter implemented in the SRF.

A. PR Controller Transfer Function

The relationship between the -components and the components is given by an anticlockwise rotating Park’s Vector

\[
i_d = i_a \cdot \cos(\omega_d t) + j \cdot i_a \cdot \sin(\omega_d t) = i_a \cdot e^{j\omega_d t},
\]

\[
i_q = i_b \cdot \cos(\omega_q t) - j \cdot i_b \cdot \sin(\omega_q t) = i_b \cdot e^{-j\omega_q t}.
\]

Thus, the influence of the Park transformation can be expressed as the Park transformation shift of all the frequencies in the frequency domain. The equivalent transfer function of the PR controller is in the SRF. \( H_{PR}(s) \) can be derived from a PI controller implemented in positive- and negative-sequence HRFs, taking into account (6) and (7).

\[
H_{PR}(s) = H_{P1}^+ + H_{P1}^-
\]

\[
H_{PR}(s) = k_p + k_i \frac{s}{s + \omega_0} + k_p + k_i \frac{s}{s - \omega_0} = 2k_p + 2k_i \frac{s}{\omega_0^2 + \omega_0^2} = K_T \cdot s
\]
For the non-ideal integrators of

$$H_I = K_I/(1 + (s/\omega_C)),$$

the PR controller transfer function takes the form

$$H_{PR}(s) = \frac{\omega_{PR} \cdot s}{s^2 + 2\omega_{PR} \cdot s + \omega^2}$$

(10)

Where $\omega_{PR}$ is the cutoff frequency, representing the limits of the integrator. In this paper, several HPR controllers are added in a cascade to control several harmonics simultaneously. The current controller takes its final form

$$H_{PR}(s) = K_P + \sum_{h=3,7...} K_{Ih} \frac{\omega_{PR} \cdot s}{s^2 + 2\omega_{PR} \cdot s + (h \cdot \omega)^2}$$

(11)

Equation (9) describes an ideal PR controller with infinite gain at the tuned frequency and no phase shift and gain at the other frequencies. The disadvantage of such a controller in practical applications is the possible stability problem associated with infinite gain, which can be avoided by using non-ideal integrators: (10). Another feature is the very narrow bandwidth of the ideal PR controllers, which makes them highly sensitive toward slight frequency variation in a typical power system.

**B. Control Algorithm**

Fig.4 shows the proposed control scheme, which includes harmonic detection (Fig. 4.5), the PR-current regulation dc

![Fig.4. Control block diagram of the HAPF.](image)

**C. DC Bus Voltage Control**

Proper control of the dc bus voltage is essential for the operation of this HAPF. The principle of controlling the dc bus voltage is based on active power control, that is, charging the dc capacitor with active power will increase the voltage while releasing a certain amount of active power, will decrease it. According to -theory, a dc component in the -coordinates corresponds to the active power and,
thus, dc bus voltage control is implemented in the SRF.

As can be seen from Fig.6, the difference between the reference value and the measured and filtered actual value is applied to a PI controller, which adjusts the direct axis current (the quadrature axis is set to zero). A low-pass filter (LPF) with a cutoff frequency of 15 Hz eliminates the harmonics from the measured dc bus voltage. The resulting control signal is added to the voltage reference.

**D. Fundamental Current Diversion**

In order to achieve the minimum current rating of the active part, the fundamental-frequency filter current needs to be diverted into a parallel inductance. This is done with a simple feedforward controller, represented in Fig.7. It calculates the voltage appearing across the passive filter inductance, which would occur in the absence of the active filter, using (1). As a result, only a small fundamental frequency current is flowing through the active elements, which is required for charging the dc capacitor.

**4.3.5 Current Control Transfer Function**

Fig.8. shows the main current control block diagram of the filter. The harmonic content of the system current is filtered from the measured system current.

The transfer function for the harmonic-detecting circuit can be expressed as

\[ H_{HPF} = \frac{i_{s,h}}{i_s} = \frac{s - j\omega_1}{s - j\omega_1 + \omega_C} \quad (12) \]

Where \( \omega_C \) is the cutoff angular frequency of the HPFs that extract the dc component in the coordinates, and is the fundamental angular frequency. The detected harmonic current \( i_{s,h} \) is compared with the current reference \( i_{sh,ref} \), and the difference \( e_h \) represents the input to the PR controller. This results in the production of the reference voltage \( u_{af,ref} \) to be generated by the inverter. In the real control circuit (implemented on a digital signal processor (DSP)), the output signal is inherently delayed with respect to the input signal. The time delay is represented as

\[ D = e^{-sT_s} \quad (13) \]

Where 100 s is the sampling period. The plant transfer function is defined as (see Fig. 2)

\[ H_{PL} = \frac{u_{af}}{i_s} = \frac{1}{Z_{af}} \cdot \frac{Z_{L,pf}}{Z_s + Z_{L,pf} + Z_{C,pf}} \quad (14) \]

Finally, the open-loop transfer function of the hybrid filter controller is given by

\[ H(s) = H_{PR} \cdot H_{HPF} \cdot D \cdot H_{PL} \quad (15) \]

**E. Filtering Characteristic**

The filtering characteristic of the HAPF depends primarily on the control algorithm; however, all of the algorithms have something in common. It is well known that power-electronic converters in harmonic filtering applications may be controlled to behave in a similar fashion to a passive element. As will be shown, the PR-controlled HAPF proposed herein mimics several parallel resonant circuits added in parallel and tuned to the characteristic harmonic frequencies.

The filtering characteristic can be obtained by calculating the equivalent model of the network (Fig.2). Applying Kirchhoff’s circuit laws yields

\[ i_s = \frac{u_s - i_l \cdot (Z_{L,pf} + Z_{C,pf}) + Z_{L,pf} \cdot i_{af}}{Z_s + Z_{L,pf} + Z_{C,pf}} \quad (16) \]

Letting:

\[ \begin{align*}
R_{add} &= K_f \frac{L_{L,pf}}{L_{af}}, \\
L_{add} &= K_f \frac{L_{L,pf}}{I_{af}^2 (h \cdot \omega_1)^2}, \\
C_{add} &= \frac{1}{L_{af} (h \cdot \omega_1)^2}
\end{align*} \quad (18) \]

We obtain
Equation (19) defines the filtering characteristic of the HAPF, which depends on the passive filter inductor and capacitor equivalent impedances $Z_{L,pf}$ and $Z_{C,pf}$, the system impedance $Z_S$, and the active power filter transfer function given by (11). As can be seen from Fig. 9, representing a single-phase equivalent circuit of the system with a connected HAPF, the active filter behaves as a pure resistor $R_{add} \, (\Omega)$, with several parallel $L_{add,h}C_{add,h}$ circuits added in series.

**IV. UNIT VECTOR CONTROLLER**

Basically a three phase voltage source inverter (VSI) connected in parallel with a nonlinear load at the point of common coupling through an inductor LF. The energy storage of the APF is a large capacitor located at the dc-link side of the inverter. The nonlinear load can be presented as a RL or RLC load connected to the power supply through a three-phase diode rectifier. As stated earlier, the APF must generate the harmonic currents to compensate harmonics produced by the nonlinear load and to make the supply currents sinusoidal. To fulfill these demands, the traditional control scheme requires a harmonic detector and current controller where both loops must be designed properly to achieve good control performance. However, it may cause excessive complexity in the design process.

In order to simplify the control scheme and to enhance the accuracy of the APF, an advanced control strategy is proposed. The proposed control scheme is implemented by using only the supply current ($i_{Sd}$ and $i_{Sq}$) without detecting the load current ($i_{L,abc}$) and filter current ($i_{F,abc}$). Thereby, the load current sensors and filter current sensors in the typical hybrid APF can be eliminated. And also, the harmonic current detection is omitted. Due to the absence of harmonic detection, the proposed control scheme can be implemented with only two loops: the outer voltage control and the inner current control.

**V. SIMULATION RESULTS**

The outer loop aims to keep dc-link voltage of the APF constant through a PI controller, which helps the APF deal with load variations. The output of this control loop is the reference active current in the fundamental reference frame ($i_{Sd}^*$). Meanwhile, the reference reactive current ($i_{Sq}^*$) is simply set to be zero, which ensures the reactive power provided by the power supply to be zero. And, the reactive power caused by loads is supplied by the hybrid APF. The inner loop is then used to regulate the supply current in the fundamental reference frame ($i_{S,dq}$) by using the proposed PI-VPI current controller. The output of this loop becomes the control signal ($v_{F,ab}^*$) applied to the four-switch APF which is implemented by the FSTPI. Since the current control is executed without the harmonic detector, the control performance of the APF only relies on the current controller. In the next section, the analysis and design of the proposed current controller will be presented.

Figure 11. Matlab/Simulink Model Circuit of System with constant Load.
Figure 12. Control Circuit of System with Constant Load.


Figure 15. Matlab/Simulink Model Circuit of System With 50% Step Load Decrease/ Increase.

Figure 16. Control Circuit of System With 50% Step Load Decrease/ Increase.

Figure 17. Simulated Waveforms in the Transient State - 50% Load Step Decrease / Increase For 3-Phase At a). source voltage b). source current c). load current d). compensation current e). DC link voltage.

Fig. 19. FFT Analysis of Source Current with Resonant Current Controller
THD value is 2.73%.

Fig. 20. Matlab/Simulink Model of Proposed Source Switch APF Operated under Several Control Strategies to Enhance PQ Features

Fig. 21. Matlab/Simulink Model of control circuit of proposed inverter.

Fig. 22. Simulated waveforms of Three phase source voltage, source current, load current and compensation current

Fig. 23. Source Voltage, Source Current, Load Current, Compensation Current
Fig. 23. shows the Source Voltage, Source Current, Load Current, and Compensation Current of proposed Four Switch APF operating under PI-VPI Controller.

Fig. 24. FFT Analysis of Source Current with Proposed Compensator
Fig. 24. shows the FFT Analysis of Source Current with Proposed Four Switch APF with PI-VPI control strategy get THD value is 1.99%.

Table I
Comparison in THD Analysis

<table>
<thead>
<tr>
<th>Controller Type</th>
<th>THD</th>
</tr>
</thead>
<tbody>
<tr>
<td>Resonant Current Controller</td>
<td>2.73%</td>
</tr>
<tr>
<td>Unit vector controller</td>
<td>1.99%</td>
</tr>
</tbody>
</table>
V. CONCLUSION
In this paper, a hybrid active power filter for reactive power compensation and harmonics filtering has been presented. It is composed of a small-rating VSC connected in parallel with the inductor of a shunt single-tuned passive filter. Since the rated power of the active filter is relatively low, the HAPF represents a viable solution for reactive power compensation and harmonic filtering. A PR current control scheme for selective harmonics compensation with the HAPF has been proposed. As shown, each controller acts as a resonant filter tuned to a certain harmonic frequency. The proper selection of the parameters ensures high selectivity and improves the transient performance of the HAPF. Another key feature is that each pair of harmonics, is filtered by one controller and, thus, important savings in terms of computational burden are achieved. Theoretical analysis, along with the simulation results, obtained from a real industrial network model, verifies the effectiveness of the proposed hybrid filter, which represents an excellent solution for reactive power compensation and harmonic filtering.

REFERENCES


