Multiscale Retinex Contrast Enhancement Algorithm for OLED Display

1B.Sreekanth; 2G.Vasantha & 3A.Rajani
1M.Tech (DSCE), Annamacharya Institute of Technology and Science, Tirupati, b.sreekanth286@gmail.com
2Assistant Professor, Dept of ECE, Annamacharya Institute of Technology and Science, Tirupati, vasantharoy@gmail.com
3Assistant Professor, Dept of ECE, Annamacharya Institute of Technology and Science, Tirupati, rajanirevanth446@gmail.com

Abstract:
In this project propose Multi scale retinex contrast enhancement algorithmic project for OLED Displays. All in all, MSR, that is the key component of the arranged algorithmic system, comprises of force controllable log operation and sub band astute increase administration. To begin with, we break down partner data picture to MSRs of different sub-groups, and figure a right pick up for each Multi Scale Retinex. Second, we tend to apply a coarse-to-fine power administration system, that re registers the MSRs and additions. This stride emphasizes till the objective force sparing is precisely expert. With feature groupings, the complexity levels of adjoining pictures are resolved deliberately utilizing fleeting intelligibility in order to keep away from insecure antiquities. At last, blessing numerous change abilities for information preparing. Test results demonstrate that the arranged algorithmic system gives preferred visual quality over past methods, and a predictable force sparing extent connection while not insecure ancient rarities, notwithstanding for feature successions.

Index Terms—Power consumption; contrast enhancement; OLED; multi-scale retinex

I. Introduction
Modern show panels are classified as emissive and non-emissive displays. Cathode-ray tube Plasma board and additionally the Organic light weight emitting diode square measure representative emissive displays that do not want external light sources, this will be the most desired one for the future generation. Whereas the thin-film junction transistor liquid shows (TFT-LCD) is also a non-emissive. In general, the first one is having many advantages over the second one.

Since emissive display can close up individual pixels[1][2], it will specific complete darkness and win a high distinction quantitative relation. Second, emissive shows consume less power than non-emissive ones as a result of every component tin associate degree emissive display will be severally driven and also the power consumption of the element is proportional to its magnitude. Non-emissive displays need to activate their back light despite component intensity. Thus, the OLED is thought to be the foremost promising candidate for the next-generation show which can replace the TFT-LCD displays presently dominating the business market. So large-size OLED panels might shortly be adopted in a very wider vary of devices like high definition TV (HDTV) and radical video. Note that show modules consume most of the facility in digital media devices. Therefore techniques to attenuate power consumption within the show square measure inevitably needed. Several image process techniques for power saving in show panels are projected, on the far side circuit-level power savings. Lee et al. projected a power-constrained contrast enhancement algorithmic rule (PCCE). They implemented an power-consumption model for OLED displays and enforced an objective operate that consists of the power terms. By reducing the target operate primarily based mostly on the bell-shaped optimization theory; they tried to simultaneously achieve distinction improvement and power savings.

The rest of this paper is organized as follows. Section II reviews a typical MSR and the SD-MSR which is
a basic framework of the proposed algorithm. Section III
defines a power model for OLED display and proposes
a power-constrained contrast enhancement algorithm for
video sequences as well as still images in detail. Section IV
presents
several optimization skills for real-time processing. Section V
provides intensive experimental results. Finally, Section VI
concludes this paper.

ILSUB-
BANDDECOMPOSEDMULTISCALERETINEX

In general, MSR, which is the key component of
the proposed algorithm, consists of power controllable log
operation and sub band wise gain control. First, we
decompose an input image to MSRs of different sub-bands,
and compute a proper gain for each MSR. Second, we apply
a coarse-to-fine power control mechanism, which
recomputed the MSRs and gains. This step iterates until the
target power saving is accurately accomplished. Which
jointly achieves contrast enhancement and dynamic range
compression using an adaptive weighting strategy proper for
an input image. Finally, this work present a power control
scheme for a constant power reduction ratio in video
sequences by using temporal coherence in video sequences.
Experimental results show that the proposed algorithm
provides better visual quality than previous methods, and a
consistent power-saving ratio without flickering

\[ R^{MSR}(x, y) = \sum_{n=1}^{N} w_n \cdot R_n(x, y) \]  \hspace{1cm} (2)

Where

\[ R_n(x, y) = \log I(x, y) - \log(F_n(x, y) \ast I(x, y)) \]  \hspace{1cm} (3)

Fig 1: Block diagram of the conventional SD-MSR

artifacts for video sequences. \(W_L\) and \(W_H\) denote
weighting parameters accord MSR is an extended SSR with multiple
kernel windows of different sizes. MSR output is a weighted
sum of several different SSR outputs. The MSR output for a
single spectral component can be represented as

Here, \(R_n(x, y)\) denotes a retinex output associated
with the \(n\)-th scale for an input image \(I_{(x, y)}\). Note that gain
\(W_n\) is determined so that it can satisfy the condition
\(\Sigma W_n = 1\). The symbol "\(*\)" in Equation 4 denotes the
convolution operation and \(N\) is the number of scales. \(F_n(x, y)\)
denotes a surround function and is given by

\[ F_n(x, y) = K_n e^{(x^2+y^2)/\sigma_n^2} \]  \hspace{1cm} (4)

Where \(K_n\) is determined so that \(F_n(x, y)\) can satisfy
\(\Sigma F_n(x, y) = 1.0\). Denotes the variance of the Gaussian
kernel at the \(n\)-th sub-band. Under the condition \(\sigma_n > \sigma_{n-1}\)
for every SSR, we can derive successive frequency sub-bands.

Note that a small \(\sigma_n\) is suitable for enhancing fine details,
whereas a large \(\sigma_n\) is suitable for improving tonality. Thus, it
is important to select an appropriate value of \(\sigma_n\) in the MSR.

Based on this rationale, Jang et al. proposed an SD-MSR
that consists of a modified logarithmic function, sub-band
decomposition, space varying sub-band gain, and an
automatic gain/offset control. The modified log \((m)\log\) is
defined as

\[ m \log(\frac{I(x, y)}{w}) = \begin{cases} \frac{w}{2^{\frac{\log D}{\log(D-1)}}} & \text{if } I_{xy} \leq \tau \text{ and } D = \log(I_{xy}) \text{ or } I_{xy} > \tau \text{ and } D = \log(D-\tau) \end{cases} \]  \hspace{1cm} (5)

where \(\tau\) is a user-defined threshold and \(D\) denotes an image
dynamic range. For example, \(D\) is 256 for an 8-bit image.

\[ w_L = \frac{\tau}{2^{\frac{\log D}{\log(D-1)}}} \]  \hspace{1cm} (6)

\[ w_H = \frac{\log(D+1)}{\log(D-\tau)} \]  \hspace{1cm} (7)

As a result, the \(m\log\) function of Eq. (7) enhances
the contrasts of dark regions as well as bright regions. In
this way, this work can enhance image details both in
highlights and shadows. Another feature of SD-MSR is to
decompose the modified retinex outputs into nearly non-
overlapping spectral bands. The following equation
accomplishes this sub-band decomposition:

\[ R_{n} = R_{n-1} - \epsilon \]  \hspace{1cm} (8)

As \(n\) increases, \(R_n\) corresponds to the
low frequency region more and more. Here, \(R_n\) is
computed by replacing the log of Eq. (5) with the \(m\log\)
of Eq. (7) next, the space varying sub-band gain at the
\(n\)-th subband is defined as

\[ g_n(x, y) = \left(\frac{1}{N_{R_n(x, y)}}\right)^{\frac{\epsilon}{\max + \epsilon}} \]  \hspace{1cm} (9)
Where \( \sigma_{\text{max}} = \max n \in \{1, 2, ..., N\} \sigma_n \). Also, \( E_g \) and \( E_o \) are two constants to avoid dividing by zero. In this paper, \( E_g \) and \( E_o \) are set to 0.1 and 0, respectively. \( NR_n \) denotes the normalized SSR at the \( n \)-th sub-band and is defined as

\[
NR_n(x, y) = \frac{|R_n(x, y)|}{|R_n|_{\text{max}}}
\]

(10)

Where \( |R_n|_{\text{max}} = \max R_n \) In a high spectral band of small \( n \), they make the gain difference between pixels larger, especially for the pixels with low \( NR_n(x, y) \). This is because this spectral band has large high-frequency components representing image details. Meanwhile, they lower the gain difference between pixels in a high spectral band of large \( n \) to maintain the characteristics of a natural scene. Thus, using Eq. 10, the final enhanced image \( I' \) is output as follows:

\[
I' = \sum_{n=1}^{N} g_n R_n
\]

(11)

III. THE PROPOSED SYSTEM

In this propose a power constrained enhancement algorithm for OLED show primarily based on SD-MSR. Fig. 2 describes the projected formula that consists of three stages. The primary stage coarsely reduces the power of an input image nearer to the target power with distinction improvement, and the second stage finely controls the image power such that it is so near to the target power. If the input is a video sequence, the ultimate stage adjusts the power of every image so that it is likely those of its neighbors by considering the temporal coherence of the input video sequence. The projected formula is differentiated from previous methods in the following aspects. First, to control the target power level mechanically. Second, to avoid the flickering process by keeping the facility levels of adjacent images constant for video sequences. Third, to achieve real time process of the projected formula on a general purpose graphics process unit (GPU) even for full HD video sequences.

![Fig 2: Block diagram of the proposed SA-MSR.](image)

A. Power Modeling in OLED Display

Before presenting an in depth clarification of the projected algorithmic program, we want to model power for associate OLED. Dong et al. conferred a pixel-based power model that estimates the ability consumption of OLED modules supported the red green-blue (RGB) specification of every pixel. the ability consumption of associate OLED with K pixels, i.e., \( P \) is

\[
P_{\text{OLED}} = C + \sum_{i=1}^{K} \left\{ f_R(R_i) + f_G(G_i) + f_B(B_i) \right\}
\]

where \( f_R(R_i) \), \( f_G(G_i) \), \( f_B(B_i) \) indicate the power consumption of red, green, and blue devices of the pixel, respectively and \( i \) stands for the pixel index in an image. \( C \) is a constant to account for the static power contribution made by the non-pixel part of the display, which is independent of the pixel values. In this paper, the constant \( C \) is not considered for convenience. Also, we consider only the Y-component because it dominates the entire overall power. Note that the Y-component indicates the luminance component in YUV color format. So we use the Y-component power consumption (YP) of an OLED display with \( K \) pixels.

\[
YP = \sum_{i=1}^{K} y_i^Y
\]

IV. ALGORITHM

A. Coarse Control Stage:

The mlog of conventional SD-MSR plays a role in enhancing the contrasts of highlight and shadow regions. In other words, contrast in the dark region becomes high by increasing the intensity level of the pixels in the region, and contrast in the bright region also becomes high by decreasing the intensity level of the pixels in the region. However, the increase of the intensity values in the shadow region results in the increase in power consumption for the OLED display. So, for low power consumption as well as contrast enhancement, even in the shadow region, so-called power-constrained log (plog) from the mlog of Eq. (4) as follows:

\[
P_{\text{plog}}(I(x, y)) = \begin{cases} 
\frac{\text{stlogDlog}(aI(x, y)+1)}{(b-1)\log(a)+1} - \frac{\text{mlog}(I(x, y))}{\log(a)+1} & I(x, y) \leq \tau \\
\frac{\text{mlog}(I(x, y))}{\log(a)+1} & I(x, y) > \tau 
\end{cases}
\]

(12)

Therefore, the plog of Eq. (12) has the effect of controlling the increase in power consumption while partially lowering the contrast in the dark region. If the input is a video sequence there would be another stage in this algorithm that is Power Control Stage for Video With image sequences, the \( \tau \) parameter for the current image can be derived from that of the previous image because of high
temporal correlation. So, if we know the power reduction ratio of the previous image in advance, we can determine the increase and decrease in $\tau$ for the current image. Based on his concept, we determine $\tau_0$ of the next image, i.e., $\tau_{0,\text{next}}$ from $\tau_0$ of the current image according to $\tau$. This helps the CCS in rapidly converging with a reduced number of iterations for video sequences. Since a significant change of $\tau_0$ between adjacent images may cause flickering artifacts due to contrast fluctuation, we limit $\tau_{0,\text{next}}$ to $[\tau_0-5, \tau_0+5]$.

VI EXPERIMENTAL RESULTS

The simulation of the proposed algorithm for an image is given in figure (3). There we compared the proposed algorithm with a typical linear algorithm and a power-constrained contrast enhancement proposed in terms of qualitative visual quality. First, Fig (3) is the results of several algorithms for the caps image when $P$ is 10%. Both the proposed algorithm and the PCCE achieve significant contrast enhancement compared to the linear algorithm. However, the PCCE loses the details in the yellow cap and shows too darkened shadows as seen in Fig.3(c). The proposed algorithm can enhance details while effectively preserving the overall intensity level of the input image. Figure (5) represents the power reduction ratios for a video sequence. We can observe that the proposed algorithm shows consistent power reduction ratio values, irrespectively of frame numbers. On the contrary, the PCCE causes significant fluctuations. Figure (3) is the plot function according to $\alpha$ values in CCS.

6.2 PERFORMANCE EVALUATIONS OF PROPOSED SYSTEM

6.2.1 Comparison Of The Edge Preservation Ratios

<table>
<thead>
<tr>
<th>S.N</th>
<th>Name</th>
<th>$P=10%$</th>
<th>$P=30%$</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Pcc</td>
<td>Proposed</td>
</tr>
<tr>
<td>1.</td>
<td>Big ship</td>
<td>1.41</td>
<td>2.9</td>
</tr>
<tr>
<td>2.</td>
<td>Bus</td>
<td>1.02</td>
<td>1.51</td>
</tr>
</tbody>
</table>

6.2.2 Comparison In Terms Of The EME Value

<table>
<thead>
<tr>
<th>S. N</th>
<th>Name</th>
<th>$P=10%$</th>
<th>$P=30%$</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>PCC</td>
<td>Proposed</td>
</tr>
<tr>
<td>1.</td>
<td>Big ship</td>
<td>48.0</td>
<td>73.07</td>
</tr>
<tr>
<td>2.</td>
<td>Bus</td>
<td>79.13</td>
<td>87.83</td>
</tr>
</tbody>
</table>

6.2.3 Comparison In Terms Of Sharpness Enhancement Metric
The Comparison Of Flickering Artifacts In Terms Of The F Value

<table>
<thead>
<tr>
<th>Name</th>
<th>PCCE</th>
<th>Proposed</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sn</td>
<td>SF</td>
<td>Q</td>
</tr>
<tr>
<td>1</td>
<td>0.02</td>
<td>0.99</td>
</tr>
<tr>
<td>2</td>
<td>0.02</td>
<td>0.99</td>
</tr>
</tbody>
</table>

The Output Power Reduction Ratios for Various P Values

<table>
<thead>
<tr>
<th>Name</th>
<th>Target Power P</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>10%</td>
</tr>
<tr>
<td>1</td>
<td>Big ship</td>
</tr>
<tr>
<td>2</td>
<td>Bus</td>
</tr>
</tbody>
</table>

In Video Sequences

Figure 7: Input Video

Figure 8: Output Video

The Comparison of Flickering Artifacts

Figure 9: Average Performance of Edge preserving and Time

VI. CONCLUSION

This project proposes an SD-MSR-based image processing algorithm for fine power control in OLED displays. In this designed a power-constrained log function for effective power saving in dark regions. Using the power-constrained log function for SD-MSR and an adaptive weighting strategy proper for an input image, we proposed a coarse-to-fine power control mechanism for still images. Finally, we presented a power control scheme for a constant power reduction ratio in video sequences by using temporal coherence in video sequences. Experimental results showed that the proposed algorithm provides better visual quality than previous works, and a consistent power-saving ratio without the flickering artifact even for video sequences. Specifically, the proposed algorithm provides at maximum 36% and on average 13% higher edge-preserving ratios than the state-of-the-art algorithm. In addition, we proved the possibility of real-time processing by accomplishing an entire execution time of 9 ms per 1080p image.

REFERENCES


B. Sreekanth, did his bachelor of Technology in Electrical and Electronics Engineering at Audisankara College of Engineering Technology, Gudur, Nellore and doing Master of Technology Digital Systems and Computer Electronics in Annamacharya Institute of Technology & Science, Karakambadi, Tirupati, Chittoor, Andhra Pradesh, India.

Miss. G. Vasantha received the B.Tech (ECE) from SRI VIDYANIKETHAN College of Engineering TIRUPATI, JNTU Ananthapur, India, in 2007 and M.Tech (VLSI) from SVPCET Puttur, India, in 2011. Present she is currently working as an Assistant Professor in ANNAMACHARYA institute of Technology and science, Tirupati, India. She has been active in research and published 1 international journals & attended 1 National conferences in the field of Communications.

Mrs. A. Rajani Obtain her B.Tech, Chaitanya Bharathi Institute of Technology, Gnadipet, Hyderabad and Master degree in S.V University, Tirupathi., Perusing (Ph.D) in S.V University, Tirupathi And her area of interest is Digital Image processing And Sinal Processing. She is 7 years of Teaching Experience. She is currently working as Assistant Professor in the Department of ECE, Annamacharya Institute of Technology and Sciences, Tirupati. Her research interests are in Digital Image processing and Signal Processing.